

Hardware Architecture Considerations for Embedded Machine Learning

Author: Jim Steele, VP at Knowles Corporation

Abstract:

Audio use-cases such as always-on voicewake and combining multiple microphones for noise reduction have been amenable to edge machine learning for quite some time. Knowles has continued the development of Audience digital signal processors to provide an open, low-power, embedded machine-learning processor catered to such use-cases. I will summarize some important considerations for machine learning on the edge.